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JC891 U.S. PTO

**UTILITY PATENT APPLICATION TRANSMITTAL  
(Large Entity)***(Only for new nonprovisional applications under 37 CFR 1.53(b))*Docket No.  
162/01498Total Pages in this Submission  
32**TO THE ASSISTANT COMMISSIONER FOR PATENTS**Box Patent Application  
Washington, D.C. 20231

Transmitted herewith for filing under 35 U.S.C. 111(a) and 37 C.F.R. 1.53(b) is a new utility patent application for an invention entitled:

**IQ MISMATCH CANCELLATION**

and invented by:

**Daniel YELLIN et al.**If a **CONTINUATION APPLICATION**, check appropriate box and supply the requisite information:☐ Continuation ☐ Divisional ☐ Continuation-in-part (CIP) of prior application No.: \_\_\_\_\_

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Enclosed are:

**Application Elements**

1. ☐ Filing fee as calculated and transmitted as described below
2. ☒ Specification having 20 pages and including the following:
  - a. ☒ Descriptive Title of the Invention
  - b. ☐ Cross References to Related Applications *(if applicable)*
  - c. ☐ Statement Regarding Federally-sponsored Research/Development *(if applicable)*
  - d. ☐ Reference to Microfiche Appendix *(if applicable)*
  - e. ☒ Background of the Invention
  - f. ☐ Brief Summary of the Invention
  - g. ☒ Brief Description of the Drawings *(if drawings filed)*
  - h. ☒ Detailed Description
  - i. ☒ Claim(s) as Classified Below
  - j. ☒ Abstract of the Disclosure

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32

**Application Elements (Continued)**

3. ☒ Drawing(s) *(when necessary as prescribed by 35 USC 113)*
- a. ☒ Formal                      Number of Sheets                      6
- b. ☐ Informal                      Number of Sheets                      \_\_\_\_\_
4. ☒ Oath or Declaration
- a. ☐ Newly executed *(original or copy)*                      ☒ Unexecuted
- b. ☐ Copy from a prior application (37 CFR 1.63(d)) *(for continuation/divisional application only)*
- c. ☒ With Power of Attorney                      ☐ Without Power of Attorney
- d. ☐ DELETION OF INVENTOR(S)  
Signed statement attached deleting inventor(s) named in the prior application,  
see 37 C.F.R. 1.63(d)(2) and 1.33(b).
5. ☐ Incorporation By Reference *(usable if Box 4b is checked)*  
The entire disclosure of the prior application, from which a copy of the oath or declaration is supplied under  
Box 4b, is considered as being part of the disclosure of the accompanying application and is hereby  
incorporated by reference therein.
6. ☐ Computer Program in Microfiche *(Appendix)*
7. ☐ Nucleotide and/or Amino Acid Sequence Submission *(if applicable, all must be included)*
- a. ☐ Paper Copy
- b. ☐ Computer Readable Copy *(identical to computer copy)*
- c. ☐ Statement Verifying Identical Paper and Computer Readable Copy

**Accompanying Application Parts**

8. ☐ Assignment Papers *(cover sheet & document(s))*
9. ☐ 37 CFR 3.73(B) Statement *(when there is an assignee)*
10. ☐ English Translation Document *(if applicable)*
11. ☐ Information Disclosure Statement/PTO-1449                      ☐ Copies of IDS Citations
12. ☐ Preliminary Amendment
13. ☒ Acknowledgment postcard
14. ☐ Certificate of Mailing
- ☐ First Class                      ☐ Express Mail *(Specify Label No.):* \_\_\_\_\_

**UTILITY PATENT APPLICATION TRANSMITTAL**  
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32

**Accompanying Application Parts (Continued)**

15. ☐ Certified Copy of Priority Document(s) *(if foreign priority is claimed)*

16. ☐ Additional Enclosures *(please identify below):*

**Fee Calculation and Transmittal**

**CLAIMS AS FILED**

For	#Filed	#Allowed	#Extra	Rate	Fee
Total Claims	37	- 20 =	17	x \$18.00	\$306.00
Indep. Claims	6	- 3 =	3	x \$78.00	\$234.00
Multiple Dependent Claims (check if applicable) <input type="checkbox"/>					\$0.00
BASIC FEE					\$690.00
OTHER FEE (specify purpose) _____					\$0.00
TOTAL FILING FEE					\$1,230.00

- ☐ A check in the amount of \_\_\_\_\_ to cover the filing fee is enclosed.
- ☐ The Commissioner is hereby authorized to charge and credit Deposit Account No. \_\_\_\_\_ as described below. A duplicate copy of this sheet is enclosed.
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  - ☐ Charge any additional filing fees required under 37 C.F.R. 1.16 and 1.17.
  - ☐ Charge the issue fee set in 37 C.F.R. 1.18 at the mailing of the Notice of Allowance, pursuant to 37 C.F.R. 1.311(b).

  
Signature

Dated: July 20, 2000

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## **IQ MISMATCH CANCELLATION**

### **BACKGROUND OF THE INVENTION**

Many transmitters transmit digital information values that are generated in base band. The base band digital values are modulated onto a carrier high frequency signal and the combined signal is amplified before its transmission. The base band values may be complex values having real and imaginary components which are traditionally referred to as I and Q components, respectively. In the modulation of the base band signal and amplification of the modulation signal, inaccuracies are introduced. These inaccuracies may cause the transmitter to interfere with signals on carrier frequencies allocated to other transmitters and therefore should be at least partially canceled by the transmitter.

One of these inaccuracies, which is due mainly to an IQ modulator of the transmitter, is referred to as IQ mismatch which is generally modeled by the following equation:

$$\begin{pmatrix} \bar{I}_q \\ \bar{Q}_q \end{pmatrix} = \begin{bmatrix} \cos(\varphi) & \sin(-\varphi) \\ b \sin(-\varphi) & b \cos(\varphi) \end{bmatrix} \begin{pmatrix} I_d \\ Q_d \end{pmatrix} \quad (1)$$

in which  $I_d$ ,  $Q_d$  are the real and imaginary input base band values of the modulator,  $I_q$ ,  $Q_q$  are the real and imaginary base band equivalent output values of the modulator and  $(\varphi, b)$  are constants which describe the IQ mismatch of the modulator. This inaccuracy may be compensated for by a mismatch correction multiplier. It is required, however, to estimate the parameters of the mismatch correction multiplier.

Another inaccuracy is due to the non-linearity of the amplification of the transmitted signal. The non-linearity may be compensated for by a predistorter.

### **BRIEF DESCRIPTION OF FIGURES**

Particular embodiments of the invention will be described with reference to the following description of the embodiments, in conjunction with the figures, wherein identical structures, elements or parts which appear in more than one figure are labeled with a same or similar number in all the figures in which they appear, and in which:

Figs. 1A and 1B are a schematic block diagram of a transmitter, in accordance with an embodiment of the present invention;

Fig. 2 is a schematic functional block diagram of an apparatus which includes mismatch cancellation circuits, in accordance with an embodiment of the present invention;

Fig. 3 is a flowchart of the actions performed by a mismatch trainer, in accordance with an embodiment of the present invention;

Fig. 4 is a flowchart of the actions performed in estimating corrected parameter values for mismatch cancellation, in accordance with an embodiment of the present invention; and

Fig. 5 is a graph comparing the performance of a transmitter in accordance with an embodiment of the present invention and a similar transmitter which may not perform IQ mismatch cancellation.

### DETAILED DESCRIPTION OF EMBODIMENTS

Figs. 1A and 1B are a schematic block diagram of a transmitter 30, in accordance with an exemplary embodiment of the present invention. Transmitter 30 comprises a transmission modulation path 33 which receives bits and shapes them for transmission over an antenna 25.

In an exemplary embodiment of the invention, modulation path 33 includes a  $\frac{\pi}{4}$ DQPSK (Differential Quadrature Phase Shift Keying) modulator 32, a first interpolator 36, a second interpolator 38, a predistorter 80, a mismatch multiplier 40, a third interpolator 42, a digital to analog converter (DAC) 44, a reconstruction filter (RCF) 46, an IQ modulator 48 and a power amplifier 23.

A directional coupler 52 senses the signals transmitted by amplifier 23 and passes them through a reverse conversion unit 55 which substantially reverses the operation of an ending portion of modulation path 33, bringing the signals back to a state in which they can be compared to the signals provided by second interpolator 38. In some embodiments of the invention, IQ modulator 48 and conversion unit 55 may be included in a single, optionally standard, chip. It is noted that in some embodiments of the invention, transmitter 30 does not have any additional reverse conversion paths which convert, to base band form, signals from after modulator 48 and/or DAC 44.

In an exemplary embodiment, reverse conversion unit 55 comprises an IQ demodulator 54 (corresponding to modulator 48), a filter 56, a sample and hold (S/H) 58, and an analog to digital converter (ADC) 60 (which together correspond to DAC 44) and a mismatch multiplier 62 (corresponding to multiplier 40).

In some embodiments of the invention, the signals from reverse conversion unit 55 are provided to a predistorter trainer 70 and to a mismatch trainer 77 together with the samples from second interpolator 38. In some embodiments of the invention, the samples from second interpolator 38 are passed through a delay unit 68 which delays them for substantially the time required for the samples to pass through path 33 and return through reverse conversion unit

55. Thus, trainers 70 and 77 receives pairs of values representing the same sample, a vector  $V_m$  from interpolator 38 and a vector  $V_f$  from after the return through conversion unit 55.

Predistorter trainer 70 uses vectors  $V_m$  and  $V_f$  to monitor values of a look up table (LUT) 84 of predistorter 80 such that the predistorter compensates for non-linearities of modulation path 33, especially of amplifier 23. The compensation is performed, for example, by multiplying the values provided to IQ modulator 48 by respective values from LUT 84. In an exemplary embodiment, trainer 70 operates using a two stage method, including an initial value assignment stage and an adaptive stage. During the initial stage trainer 70 accumulates a plurality of pairs of values which represent the non-linearity and optionally uses the accumulated pairs in creating a polynomial model of the non-linearities. The values of LUT 84 are adjusted based on the polynomial model. Mismatch trainer 77 assigns digital values to correction 2x2 matrices B1 (41) and B2 (63) which are optionally used by mismatch multipliers 40 and 62 in canceling the mismatch effects of modulator 48 and demodulator 54, respectively. In some embodiments of the present invention, mismatch trainer 77 determines the values to be assigned to matrices 41 and 63 based on the received pairs of vectors  $V_m$  and  $V_f$ . In some embodiments of the invention, the digital values in LUT 84 are close to the values required to compensates for the non-linearities, before mismatch trainer 77 begins to operate. In these embodiments, mismatch trainer 77 may be planned to operate based on the assumption that the mismatch errors in transmitter 30 are relatively small.

Fig. 2 is a schematic functional block diagram of elements relevant to mismatch cancellation in an apparatus 300 which includes mismatch cancellation circuits, in accordance with an embodiment of the present invention. Apparatus 300 may be for example a communication device, such as transmitter 30 of Figs. 1A and 1B, a receiver or any other apparatus which suffers from IQ mismatch. A vector of signals  $\bar{V}_m = (I_m, Q_m)$ , formed of a real part  $I_m$  and an imaginary part  $Q_m$  is provided to a sequence of non-linear elements 320, 330 and 340 which includes a signal processing path. One or more of the non-linear elements, e.g., elements 320 and 340, cause IQ mismatch distortions which are corrected by respective IQ correction units 322 and 342. The distortions of non-linear elements 320 and 340 are modeled in accordance with equation (1), with unknown parameters  $(\phi_1, b_1)$  and  $(\phi_2, b_2)$ , respectively, replacing  $(\phi, b)$ . Non-linear elements 320 and 340, may be, for example, IQ modulator 48 and IQ demodulator 54 of transmitter 30.

In some embodiments of the invention, IQ correction units 322 and 342 comprise respective multipliers 324 and 344 which correct the distortions of the respective

modulator/demodulator. Multipliers 324 and 344 multiply the signals passing through them by matrices 326 and 346, respectively, which have the form:

$$B_i = \frac{1}{\cos(2\theta_i)} \begin{bmatrix} \cos(\theta_i) & \beta_i \sin(\theta_i) \\ \sin(\theta_i) & \beta_i \cos(\theta_i) \end{bmatrix} \quad i = 1, 2 \quad (2)$$

in which  $(\theta_1, \beta_1, \theta_2, \beta_2)$  are parameters which are set by a mismatch trainer 377. In some embodiments of the invention, parameters  $(\theta_1, \beta_1, \theta_2, \beta_2)$  are estimated, as described hereinbelow, to bring their values as close as possible to the distortions of non-linear elements 320 and 340, represented in the following description by matrices  $A_i$ . When parameters  $(\theta_i, \beta_i)_{i=1,2}$  are substantially equal to  $(\varphi_i, \frac{1}{b_i})$ , respectively, the distortions of non-linear elements 320 and 340 are substantially completely compensated for, as  $A_i B_i \approx I$  ( $I$  being the 2x2 identity matrix) for  $i=1,2$ .

Optionally, the vectors  $\bar{V}_m$  are predistorted by a predistorter 380 into  $\bar{V}_d = (I_d, Q_d)$  using fixed values from a LUT 384, before the vectors are provided to non-linear element 320.

In some embodiments of the invention, non-linear element 330 has a complex-valued base band equivalent transfer function which is optionally modeled by:

$$G_{PA} = \begin{bmatrix} g_I & -g_Q \\ g_Q & g_I \end{bmatrix} \quad (3)$$

in which  $g_I, g_Q$  are the real and imaginary components, respectively, of the complex valued gain of non-linear element 330. It is noted that generally,  $g_I, g_Q$  depend on the input signal amplitude of non-linear element 330.

Thus, non-linear element 340 provides at its output vectors  $\bar{V}_f = (I_f, Q_f)$  which are related to vectors  $V_d$  according to:

$$V_f = B_2 A_2 G_{PA} A_1 B_1 V_d \quad (4)$$

In some embodiments of the invention, as described hereinbelow in detail with respect to Figs. 3 and 4, mismatch trainer 377 iteratively adjusts the values of parameters  $(\theta_1, \beta_1, \theta_2, \beta_2)$  based on vector pairs  $(V_m, V_f)$  handled by apparatus 300 during adjustment periods. In each of one or more repetitions of the iterative adjustment performed by mismatch trainer 77, the trainer estimates a correction value of one or more of parameters  $(\theta_1, \beta_1, \theta_2, \beta_2)$  which minimizes the mismatch error not corrected by multipliers 324 and 344.

It is noted that the vectors  $V_f$  received by mismatch trainer 77 are altered relative to the respective vectors  $V_d$  by a plurality of non-linear elements which have complex valued transfer functions, e.g., a modulator, a demodulator and a power amplifier. In some embodiments of the invention, the non-linear elements comprise RF elements.

Fig. 3 is a flowchart of the actions performed by mismatch trainer 377, in accordance with some embodiments of the present invention. When apparatus 300 is operated, optionally after various initialization procedures described hereinbelow, mismatch trainer 377 assigns (block 100) initial values to matrices 326 and 346, i.e., assigns values to parameters  $(\theta_1, \beta_1, \theta_2, \beta_2)$ , referred to cumulatively as  $\Phi$ . In an embodiment of the invention, the initial values of matrices 326 and 346 are predetermined values which are calibrated in the manufacturing process of apparatus 300. Alternatively or additionally, the initial values are the values most recently calculated the last time apparatus 300 was operated. Further alternatively or additionally, the initial values are taken as a weighted average of values calculated in previous operations of apparatus 300 and/or of factory calibrated values.

After the initial values of matrices 326 and 346 are assigned (100), mismatch trainer 377 accumulates (102) a predetermined number of vector pairs  $(V_m, V_f)$  of signals handled by apparatus 300 and combines them into combined vectors denoted by  $V_f$  and  $V_m$ , respectively.

In some embodiments of the invention in which apparatus 300 comprises a transmitter, the predetermined number of accumulated pairs  $(V_m, V_f)$  comprises the pairs of a single transmission slot, which in an exemplary embodiment includes 140 pairs  $(V_m, V_f)$ . Mismatch trainer 377 then estimates (104) corrected values of parameters  $\Phi = (\theta_1, \beta_1, \theta_2, \beta_2)$  which minimize the cost function:

$$C(\Phi) = |V_f(\Phi)^j - KV_m^j|^2 \quad (5)$$

(in which K is a desired gain of signals at the output of non-linear element 330) for the accumulated vector pairs  $(V_m, V_f)$ . An optional method for estimating the corrected values is described below with reference to Fig. 4. The estimated values of parameters  $\Phi = (\theta_1, \beta_1, \theta_2, \beta_2)$  are then used to change (106) the values of matrices 326 and 346.

In some embodiments of the invention, cost functions other than defined by equation (5) are used, e.g., cost functions with other distance definitions than  $|x|^2$ , such as the absolute size of the difference vector or the maximal difference among the accumulated pairs  $(V_m, V_f)$ .

In some embodiments of the invention, the accumulation (102), estimation (104) and value assignment (106) are repeated for a predetermined number of repetitions. In an



exemplary embodiment of the invention, the accumulation (102), estimation (104) and value assignment are repeated for about 10 repetitions. The number of repetitions may depend on the processing power of a processor performing the tasks of mismatch trainer 377 and the time required for the processor to handle tasks other than IQ mismatch cancellation. Alternatively, the accumulation (102), estimation (104) and value assignment (106) are repeated until a sufficient accuracy is achieved, for example, until the cost function described by equation (5) is beneath a predetermined level.

Alternatively to adjusting all of parameters  $(\theta_1, \beta_1, \theta_2, \beta_2)$  concurrently, during some or all of the repetitions of accumulation (102), estimation (104) and value assignment (106) only a sub group of parameters  $(\theta_1, \beta_1, \theta_2, \beta_2)$  are adjusted while others are kept constant during that repetition. In one embodiment of the invention, each parameter is adjusted in separate repetitions and all the other parameters are kept constant. In an exemplary embodiment of the invention, during a first sequence of one or more repetitions, parameters  $(\theta_1, \beta_1)$  are adjusted and during a second sequence of one or more repetitions, parameters  $(\theta_2, \beta_2)$  are adjusted. Optionally, additional repetitions are performed thereafter, in which parameters  $(\theta_1, \beta_1)$  and/or  $(\theta_2, \beta_2)$  are re-adjusted, for example, alternately. In an exemplary embodiment of the present invention, the total number of repetitions are divided equally between the different parameters. Alternatively, more repetitions are performed for a first group of parameters than for a second group of parameters, for example, when the inaccuracy in one of the elements causing the IQ mismatch (e.g., non-linear element 340) is larger than the inaccuracy in another element which causes the mismatch.

It is noted that the groups of parameters adjusted in different repetitions may include common parameters from  $(\theta_1, \beta_1, \theta_2, \beta_2)$ .

In an embodiment of the invention, each parameter is adjusted separately until the value of cost equation (5) goes down by at least a predetermined percent and/or a predetermined number of repetitions are performed. Mismatch trainer 377 then moves on to a different parameter until all the parameters are adjusted. Mismatch trainer 377 then re-adjusts the previously adjusted parameters in the same or in a different order, until cost equation (5) reaches a desired level and/or a predetermined number of allowed repetitions has occurred.

After the predetermined number of repetitions have been performed and/or the predetermined cost level is reached, mismatch trainer 377 optionally stores (108) the

parameter values to be used in assigning (100) the initial values in a later mismatch training of apparatus 300.

Alternatively to accumulating (102), for each repetition, vector pairs ( $V_m, V_f$ ) of symbols handled in a single slot, a greater or lesser number of vector pairs ( $V_m, V_f$ ) may be accumulated for use in each repetition. For example, each repetition may be based on vector pairs of two slots, or half a slot. In some embodiments of the invention, each repetition is performed for a single pair ( $V_m, V_f$ ). In some embodiments of the invention, different numbers of vector pairs are accumulated for different repetitions. In some embodiments of the invention, a large number of pairs are accumulated for the early repetitions which require relatively large adjustments and a smaller number of pairs is accumulated for later adjustments. Alternatively or additionally, different repetitions are used to adjust different numbers of parameters, and the number of accumulated pairs depends on the number of adjusted parameters.

Fig. 4 is a flowchart of the actions performed in estimating (104) corrected values of parameters  $\underline{\Phi} = (\theta_1, \beta_1, \theta_2, \beta_2)$ , in accordance with an embodiment of the present invention. In embodiments of the invention, mismatch trainer 377 approximates (120) unknown matrices  $A_i$  based on the following equation:

$$\hat{A}_i \approx B_i^{-1} = \begin{bmatrix} \beta_i \cos(\theta_i) & -\beta_i \sin(\theta_i) \\ -\sin(\theta_i) & \cos(\theta_i) \end{bmatrix} \quad (6)$$

in accordance with the assumption that the current values of parameters  $(\theta_1, \beta_1, \theta_2, \beta_2)$  are very close to  $(\varphi_1, \frac{1}{b_1}, \varphi_2, \frac{1}{b_2})$ , respectively, and therefore  $A_i B_i \approx I$  for  $i=1,2$ .

In an embodiment of the invention, the above assumption is considered fulfilled when the maximal mismatch of non-linear elements 320 and/or 340 is about  $\pm 0.1$  dB and  $\pm 1^\circ$ , regardless of the values of parameters  $(\theta_1, \beta_1, \theta_2, \beta_2)$ . Alternatively or additionally, the above assumption is considered fulfilled when the maximal mismatch of non-linear elements 320 and/or 340 is about  $\pm 0.3$  dB and  $\pm 3^\circ$ , regardless of the values of parameters  $(\theta_1, \beta_1, \theta_2, \beta_2)$ . Further alternatively or additionally, factory measurements of parameters  $(\theta_1, \beta_1, \theta_2, \beta_2)$  are performed to a sufficient accuracy such that the above assumption is substantially always true, given the initial values of the parameters. Further alternatively or additionally, the method of Fig. 4 is used even in those cases in which the above assumption is not true.

For each of the accumulated vector pairs  $(V_m, V_f)$ , mismatch trainer 377 estimates (124) the value of  $g_I, g_Q$  for matrix  $G_{PA}$  (equation (3)). In an embodiment of the invention, for estimating  $g_I, g_Q$  it is assumed that  $A_i B_i \approx I$  for  $i=1,2$ . Therefore, equation (4) can be stated as  $V_f \approx G_{PA} V_d$ , leading to an estimation of  $G_{PA}$  given by:

$$\hat{G}_{PA} = \frac{V_f V_d^*}{|V_d|^2} \quad (7)$$

wherein  $*$  denotes the complex conjugate operator. In an embodiment of the invention,  $V_d$  is calculated (122) from  $V_m$  using the contents of LUT 384 (Fig. 2). Alternatively, mismatch trainer 377 receives the respective value of  $V_d$  together with each vector pair  $(V_m, V_f)$ .

In embodiments of the invention, for each vector pair  $(V_m, V_f)$ , mismatch trainer 377 evaluates (126) the partial derivatives  $\left( \frac{\partial \mathcal{V}_f}{\partial \Phi^i} \right)$  of the accumulated vectors  $V_f$ , forming a combined vector  $\left( \frac{\partial \mathcal{V}_f}{\partial \Phi^i} \right)$  which includes the partial derivatives of the elements of combined vector  $V_f$ , according to each one of the desired parameters. In an embodiment of the invention, mismatch trainer 377 evaluates (126) the partial derivatives using the following equations (8a-8d) which show  $\frac{\partial \mathcal{V}_f}{\partial \Phi^i}$  for each of the parameters included in  $\Phi = (\theta_1, \beta_1, \theta_2, \beta_2)$ .

$$\frac{\partial \mathcal{V}_f}{\partial \theta_1} = \begin{bmatrix} \cos(\theta_2) & \beta_2 \sin(\theta_2) \\ -\sin(\theta_2) & \beta_2 \cos(\theta_2) \end{bmatrix} [\hat{G}_1] \begin{bmatrix} -\sin(\theta_1) & \beta_1 \cos(\theta_1) \\ \cos(\theta_1) & -\beta_1 \sin(\theta_1) \end{bmatrix} \bar{V}_d \quad (8a)$$

$$\frac{\partial \mathcal{V}_f}{\partial \beta_1} = \begin{bmatrix} \cos(\theta_2) & \beta_2 \sin(\theta_2) \\ \sin(\theta_2) & \beta_2 \cos(\theta_2) \end{bmatrix} [\hat{G}_1] \begin{bmatrix} 0 & \sin(\theta_1) \\ 0 & \cos(\theta_1) \end{bmatrix} \bar{V}_d \quad (8b)$$

$$\frac{\partial \mathcal{V}_f}{\partial \theta_2} = \begin{bmatrix} -\sin(\theta_2) & \beta_2 \cos(\theta_2) \\ \cos(\theta_2) & -\beta_2 \sin(\theta_2) \end{bmatrix} [\hat{G}_1] \begin{bmatrix} \cos(\theta_1) & \beta_1 \sin(\theta_1) \\ \sin(\theta_1) & \beta_1 \cos(\theta_1) \end{bmatrix} \bar{V}_d \quad (8c)$$

$$\frac{\partial \mathcal{V}_f}{\partial \beta_2} = \begin{bmatrix} 0 & \sin(\theta_2) \\ 0 & \cos(\theta_2) \end{bmatrix} [\hat{G}_1] \begin{bmatrix} \cos(\theta_1) & \beta_1 \sin(\theta_1) \\ \sin(\theta_1) & \beta_1 \cos(\theta_1) \end{bmatrix} \bar{V}_d \quad (8d)$$

In equations (8a-8d), matrix  $\hat{G}_1$  is an approximation of  $G_1 = A_2 G_{PA} A_1$  using the approximations of equations (6) and (7). It is noted that  $V_f$  includes a term of the form  $\frac{1}{\cos(2\theta_1)\cos(2\theta_2)}$  (due to matrices  $B_i$ ) which was ignored in generating equations (8a-8d). In

an embodiment of the invention, the effect of this term is included in the step size  $\mu$  of the

iteration equation (10) described in the following paragraph. Alternatively, in some embodiments, corrected equations taking into account the effect of this term are used instead of equations (8a-8d).

The values of the combined vectors  $\underline{V}_f, \underline{V}_m$  and  $\left(\frac{\partial \mathcal{V}_f}{\partial \Phi^i}\right)$  are used to calculate the

5 correction step of parameters  $\underline{\Phi}=(\theta_1, \beta_1, \theta_2, \beta_2)$ . In embodiments of the invention, the correction steps of the parameters are calculated by inserting (127) the combined vectors into the partial derivatives of the cost function of equation (5):

$$\frac{\partial \mathcal{V}_f}{\partial \Phi^i} = 2\Re\left\{\left(\frac{\partial \mathcal{V}_f}{\partial \Phi^i}\right)^H [\underline{V}_f - K\underline{V}_m]\right\} \quad (9)$$

10 in which H denotes the Hermitian operator, and K is a desired gain of signals at the output of non-linear element 330. In some embodiments of the invention, K has a predetermined known value. Alternatively, K is estimated by mismatch trainer 377 (or by any other element of apparatus 300) from pairs  $(\underline{V}_m, \underline{V}_f)$  using methods known in the art.

In an exemplary embodiment of the present invention, parameters  $\underline{\Phi}=(\theta_1, \beta_1, \theta_2, \beta_2)$  are then adjusted (128) using the steepest descent vector iteration equation:

$$15 \quad \underline{\Phi}^{i+1} = \underline{\Phi}^i - \mu \frac{\partial \mathcal{V}_f}{\partial \Phi^i} \quad (10)$$

in which  $\mu$  is a predetermined step size.

In an embodiment of the invention,  $\mu$  has a constant value throughout the mismatch cancellation of apparatus 300. Alternatively, different values of  $\mu$  may be used for at least some of the different repetitions of the method of Fig. 3. In an exemplary embodiment of the invention, a value of  $\mu=0.2$  is used in repetitions in which parameters for cancellation of the mismatch of non-linear element 320 are adjusted, and  $\mu=0.31$  is used for repetitions in which parameters for cancellation of the mismatch of non-linear element 340 are adjusted. Alternatively or additionally, different values of  $\mu$  are used for different pairs  $(\underline{V}_m, \underline{V}_f)$  during a single repetition, i.e.,  $\mu$  depends on j in equation (10). Further alternatively or additionally, 20 different values of  $\mu$  are used for different ones of parameters  $\underline{\Phi}=(\theta_1, \beta_1, \theta_2, \beta_2)$ .

In an alternative embodiment of the invention, the estimation (124) of matrix  $G_{PA}$  is performed using an average value of all the vectors  $(\underline{V}_m, \underline{V}_f)$  accumulated 102 during the current repetition. Thus, the estimate of matrix  $G_{PA}$  does not depend on a single vector which may be an outlier. In embodiments of the invention of this alternative, for each repetition of

accumulation (102), estimation (104) and value assignment (106) the estimation of matrix  $G_{PA}$  is performed only once as it does not depend on specific values of  $(V_m, V_f)$ .

Alternatively to using the steepest descent iteration equation (equation (10)), other iteration equations as are known from optimization theory, such as the conjugate gradient equation and the quasi-Newton equation, may be used. In some embodiments of the invention, the iteration equation used is chosen as a tradeoff between the processing power of apparatus 300 and the required convergence speed of the parameters of matrices 326 and 346. If fast convergence is required and apparatus 300 has a relatively high processing power level, a fast convergence method which requires dense computation (e.g., the quasi-Newton method) may be used. If, however, low processing power utilization is more important than fast convergence, simpler methods, such as the steepest descent method, may be used.

Further alternatively, the assignment of the parameters of matrices 326 and/or 346 is performed in a single batch step, optionally based on a large amount of accumulated signals, or is performed adaptively.

Fig. 5 is a graph comparing a simulation performance of transmitter 30 using mismatch trainer 77 in accordance with an embodiment of the present invention and a similar transmitter which does not perform IQ mismatch cancellation. The transmission of transmitter 30 is performed on a channel with a bandwidth of  $\pm 21\text{KHz}$  around a carrier frequency. It is required that 50KHz away from the carrier frequency the transmission strength be sufficiently low so as not to interfere with other transmitters in the vicinity of transmitter 30. For example, the Japanese RCR27C standard requires that the adjacent channel power ratio (ACPR) of transmitters, be lower than -45dB at 50KHz away from the carrier frequency and be lower than -60dB at 100KHz away from the carrier frequency. As shown in Fig. 5, the ACPR at 50KHz away from the carrier frequency is -59.5dB for transmitter 30 and only -51dB for a transmitter without predistortion. The ACPR at 100KHz away from the carrier frequency is -66dB for transmitter 30 and only -56dB for a transmitter without predistortion.

In some embodiments of the invention, the method of adjustment of the mismatch cancellation parameters described above is performed each time transmitter 30 is switched on. In some embodiments of the invention, the mismatch cancellation parameter adjustment method is performed together with initialization methods of transmitter 30, such as delay adjustment and setting predistortion values. In some embodiments of the invention, when transmitter 30 is first operated, a first transmitted slot of symbols is used for level control and/or delay adjustment of delay unit 68. In some embodiments of the invention, the initial

values of parameters  $(\theta_1, \beta_1, \theta_2, \beta_2)$  are loaded into matrices 41 and 63 for use in the transmission of the first slot of symbols. Alternatively, the initial values of the parameters are loaded into matrices 41 and 63 after the first slot or at substantially any other time point before the beginning of the mismatch cancellation method. Second and third slots are used for setting the values of LUT 84 and ten following slots are used for IQ mismatch cancellation. Thereafter the transmitted slots are used for continuous adjustment of the values of LUT 84. Alternatively or additionally, the transmitted slots are used in any other order and/or one or more of the transmitted slots are used for a plurality of tasks.

Alternatively to performing the adjustment of the mismatch cancellation parameters in a single stage using current values of predistortion LUT 84, the method of adjustment of the mismatch cancellation parameters is performed in a plurality of stages separated by adjustment stages of the predistortion LUT.

In embodiments of the invention, the mismatch cancellation method is performed on regular data signals generated in the normal use of transmitter 30. These values are chosen without relation to the mismatch cancellation. Alternatively, a special transmission is performed for the mismatch cancellation method. Optionally, the signals of the special transmission are chosen as best suited for mismatch cancellation. In some embodiments of the invention, the signals used in the mismatch cancellation method are at the power level at which transmitter 30 normally operates.

In embodiments of the invention, in addition to, or instead of, performing mismatch cancellation when transmitter 30 is first operated, mismatch cancellation is performed periodically and/or when the transmission conditions of transmitter 30 change substantially.

Although the above described embodiments show mismatch trainer 77 as receiving the values  $V_m$ , representing the samples before they reach modulator 48, from interpolator 38, in other embodiments of the invention, these values are received from other points before predistorter 80, e.g., from interpolator 36. Alternatively or additionally, mismatch trainer 77 receives vectors  $V_d$  from after predistorter 80 instead of, or in addition to, vectors  $V_m$  from before predistorter 80. Vectors  $V_m$  may be calculated from vectors  $V_d$  by dividing by the respective value in LUT 84.

It is noted that some of the units of transmitter 30 may be implemented either in software (on one or more processors) or in hardware. In an exemplary implementation of the embodiment of Figs. 1A and 1B, modulator 32, interpolators 36 and 38, predistorter 80, mismatch multipliers 40 and 62, matrices 41 and 63, delay unit 68 and trainers 70 and 77 are

implemented in software on a single processor, such as a digital signal processor (DSP). In an exemplary embodiment, the remaining elements of transmitter 30 are implemented in hardware. Alternatively, a greater or lesser number of the elements of transmitter 30 are implemented in hardware. For example, in some embodiments, interpolators 36 and/or 38 are also implemented in hardware. In an embodiment of the invention, multiplier 40 is implemented in hardware and multiplier 62 is implemented in software. Alternatively, both multipliers 40 and 62 are implemented in either hardware or in software.

Apparatus 300 on which the present invention is implemented may include wireless or non-wireless transmitters (in base stations, handsets or otherwise), receivers, amplifiers and/or any other apparatus which suffers from IQ mismatch distortions. The present invention is not limited to transmitters of any specific modulation method, but rather may be used with substantially any non-constant envelope modulation methods, including, but not limited to, MPSK, DPSK and QAM. Furthermore, the present invention is not limited to setting parameters of matrix multipliers for cancellation of IQ mismatch effects. Rather, any mismatch adjustment circuitry (e.g., analog multipliers, filters) may be used to cancel the mismatch effect and the parameters of the mismatch adjustment circuitry are adjusted in accordance with the present invention.

It is further noted that mismatch trainer 77 may receive feedback values  $V_f$  which passed through elements other than shown in Fig. 2. Particularly, feedback values  $V_f$  may pass through additional or less RF elements and/or different elements from power amplifier 23 and/or IQ demodulator 54. Furthermore, mismatch trainer 77 may assign values to more or fewer mismatch multipliers (or other mismatch adjustment circuitry). In an exemplary embodiment of the invention, mismatch trainer 77 assigns values only to a mismatch multiplier of modulator 48.

It will be appreciated that the above described methods may be varied in many ways, including, changing the order of steps, and the exact implementation used. It should also be appreciated that the above described description of methods and apparatus are to be interpreted as including apparatus for carrying out the methods and methods of using the apparatus.

The present invention has been described using non-limiting detailed descriptions of embodiments thereof that are provided by way of example and are not intended to limit the scope of the invention. It should be understood that features described with respect to one embodiment may be used with other embodiments and that not all embodiments of the

invention have all of the features shown in a particular figure or described with respect to one of the embodiments. Variations of embodiments described will occur to persons of the art.

Structure and acts described herein are replaceable by equivalents which perform the same function, even if the structure or acts are different, as known in the art. Therefore, the scope of the invention is limited only by the elements and limitations as used in the claims. When used in the following claims, the terms "comprise", "include", "have" and their conjugates mean "including but not limited to".



**CLAIMS**

1. A method of assigning values to parameters for IQ mismatch cancellation, comprising:  
providing values to a processing path of a communication device;  
5 passing the provided values through an IQ modulator, at least one non-linear element  
and an IQ demodulator, to produce distorted values; and  
estimating at least one parameter for cancellation of IQ mismatch effects of the  
communication device responsive to the provided values and to the distorted values.
- 10 2. A method according to claim 1, wherein estimating the at least one parameter  
comprises estimating the at least one parameter responsive to the provided values and to the  
distorted values, without additional values passed through a sub-group of the IQ modulator,  
the at least one non-linear element and the IQ demodulator.
- 15 3. A method according to claim 1, wherein estimating the at least one parameter  
comprises estimating at least one parameter, respectively, for each of the IQ modulator and the  
IQ demodulator.
4. A method according to claim 1, wherein passing the values through the IQ modulator  
20 and the IQ demodulator comprises passing the values through the IQ modulator in the  
processing path and through an IQ demodulator in a reverse path of the communication device.
5. A method according to claim 1, wherein passing the values through at least one non-  
linear elements comprises passing the values through a power amplifier.
- 25 6. A method according to claim 1, wherein providing values to a processing path of a  
communication device comprises providing values to a processing path of a transmitter.
7. A method according to claim 1, wherein estimating the at least one parameter  
30 comprises accumulating a predetermined number of pairs of provided values and respective  
distorted values and estimating from the accumulated pairs.

8. A method according to claim 7, wherein the communication device comprises a transmitter and accumulating a predetermined number of pairs comprises accumulating a number of pairs transmitted during a single transmission slot.

9. A method according to claim 1, wherein passing the values through elements to produce distorted values comprises passing also through at least one multiplier which performs IQ mismatch cancellation based on current values of the at least one parameter which is to be estimated.

10. A method according to claim 9, wherein estimating the at least one parameter comprises determining a next step correction of the current value of the at least one parameter.

11. A method according to claim 1, comprising repeating the estimating of the at least one parameter a predetermined number of repetitions.

12. A method according to claim 1, comprising retrieving from storage initial values of the at least one parameter.

13. A method according to claim 1, comprising storing the estimated value of the at least one parameter for later use.

14. A method according to claim 1, wherein providing the values to the processing path comprises providing values generated for the IQ mismatch cancellation method.

15. A method according to claim 1, wherein providing the values to the processing path comprises providing values generated without relation to the IQ mismatch cancellation method.

16. A method according to claim 1, wherein passing the provided values through the at least one non-linear element comprises passing the values through at least one non-linear element which has a gain which depends on the magnitude of its input signal.

17. A method of assigning values to parameters for IQ mismatch cancellation, comprising:

estimating a complex-valued base band equivalent gain of an amplification unit of a communication device;

approximating values of one or more matrices representing an IQ mismatch effect of the communication device based on current values of at least one parameter for cancellation of the IQ mismatch effect; and

selecting a next step value of the at least one parameter for cancellation of the IQ mismatch effect, which next step values minimize a cost function which depends on values passed through the communication device and depends on the estimated complex-valued gain and the approximated one or more matrices.

18. A method according to claim 17, wherein estimating the complex valued gain comprises estimating separately for each value passed through the communication device.

19. A method according to claim 17, wherein estimating the complex valued gain comprises estimating once for a plurality of accumulated values passed through the communication device.

20. A trainer for an IQ mismatch cancellation unit of a communication device including at least an IQ modulator and an IQ demodulator along a sequential path, comprising:

a first input adapted to receive values from before an entrance to the first of the IQ modulator and the IQ demodulator along the sequential path;

a second input adapted to receive values from after an output of the last of the IQ modulator and the IQ demodulator along the sequential path; and

a determination unit adapted to determine at least one parameter of the cancellation unit responsive to values on both the first and second inputs.

21. A trainer according to claim 20, wherein the trainer does not have inputs from any point between the IQ modulator and the IQ demodulator.

22. A trainer according to claim 20, wherein the sequential path includes a non-linear element between the IQ modulator and the IQ demodulator.

23. A trainer according to claim 20, wherein the determination unit is adapted to determine a corrected value of the at least one parameter which minimizes a cost function based on values received from both the first and second inputs.

5 24. A trainer according to claim 20, wherein the determination unit is adapted to determine at least one parameter for each of at least two cancellation units.

25. A trainer according to claim 20, wherein the determination unit is adapted to determine the at least one parameter iteratively.

10

26. A transmitter, comprising:

an IQ modulator;

an IQ demodulator adapted to receive values from after an output of the modulator;

15 mismatch adjustment circuitry adapted to compensate for mismatch effects of the modulator; and

a mismatch trainer adapted to adjust at least one parameter of the mismatch adjustment circuitry responsive to values entering the modulator and respective values from the output of the demodulator.

20 27. A transmitter according to claim 26, wherein the demodulator is part of a reverse conversion path of the transmitter.

28. A transmitter according to claim 26, comprising a power amplifier between the modulator and the demodulator.

25

29. A transmitter, comprising:

an IQ modulator;

a predistorter adapted to predistort values entering the modulator;

a predistorter trainer adapted to adjust parameters of the predistorter;

30 mismatch adjustment circuitry adapted to adjust signals entering the IQ modulator in order to cancel mismatch effects of the modulator;

a mismatch trainer adapted to adjust at least one parameter of the mismatch adjustment circuitry; and

a feedback conversion unit adapted to convert signals which passed through the IQ modulator back to base band and to provide the converted signals to the mismatch trainer and the predistorter trainer.

5 30. A transmitter according to claim 29, comprising a power amplifier adapted to receive signals from the IQ modulator and wherein the feedback conversion unit is adapted to convert to base band, signals which passed through the power amplifier.

31. A transmitter according to claim 29, wherein the feedback conversion unit comprises a demodulator and a mismatch multiplier of the demodulator and the mismatch trainer is adapted to adjust parameters of the mismatch multiplier of the demodulator.

32. A transmitter according to claim 29, wherein the mismatch adjustment circuitry comprises a matrix multiplier.

15 33. A transmitter according to claim 29, wherein the mismatch trainer is adapted to adjust the at least one parameter of the mismatch adjustment circuitry iteratively.

34. A transmitter according to claim 29, comprising a processor which includes the predistorter, the predistorter trainer, the mismatch adjustment circuitry and the mismatch trainer.

35. A method of assigning values to parameters for IQ mismatch cancellation of a transmitter, comprising:

25 transmitting values by the transmitter;  
 estimating at least one parameter for cancellation of IQ mismatch effects of the transmitter responsive to the values transmitted during a first period; and  
 adjusting the at least one parameter estimated responsive to the values transmitted during a first period responsive to values transmitted during a second period, which second  
 30 period is separated from the first period by a rest period in which the transmitted values are not used to adjust parameters for IQ mismatch cancellation.

36. A method according to claim 35, wherein the values transmitted during the rest period are used for adjusting a predistorter of the transmitter.

37. A method according to claim 36, comprising adjusting the predistorter based on values  
5 transmitted before the first period.

**ABSTRACT**

A method of assigning values to parameters for IQ mismatch cancellation. The method includes providing values to a processing path of a communication device, passing the provided values through an IQ modulator, at least one non-linear element and an IQ demodulator, to produce distorted values, and estimating at least one parameter for cancellation of IQ mismatch effects of the communication device responsive to the provided values and to the distorted values.

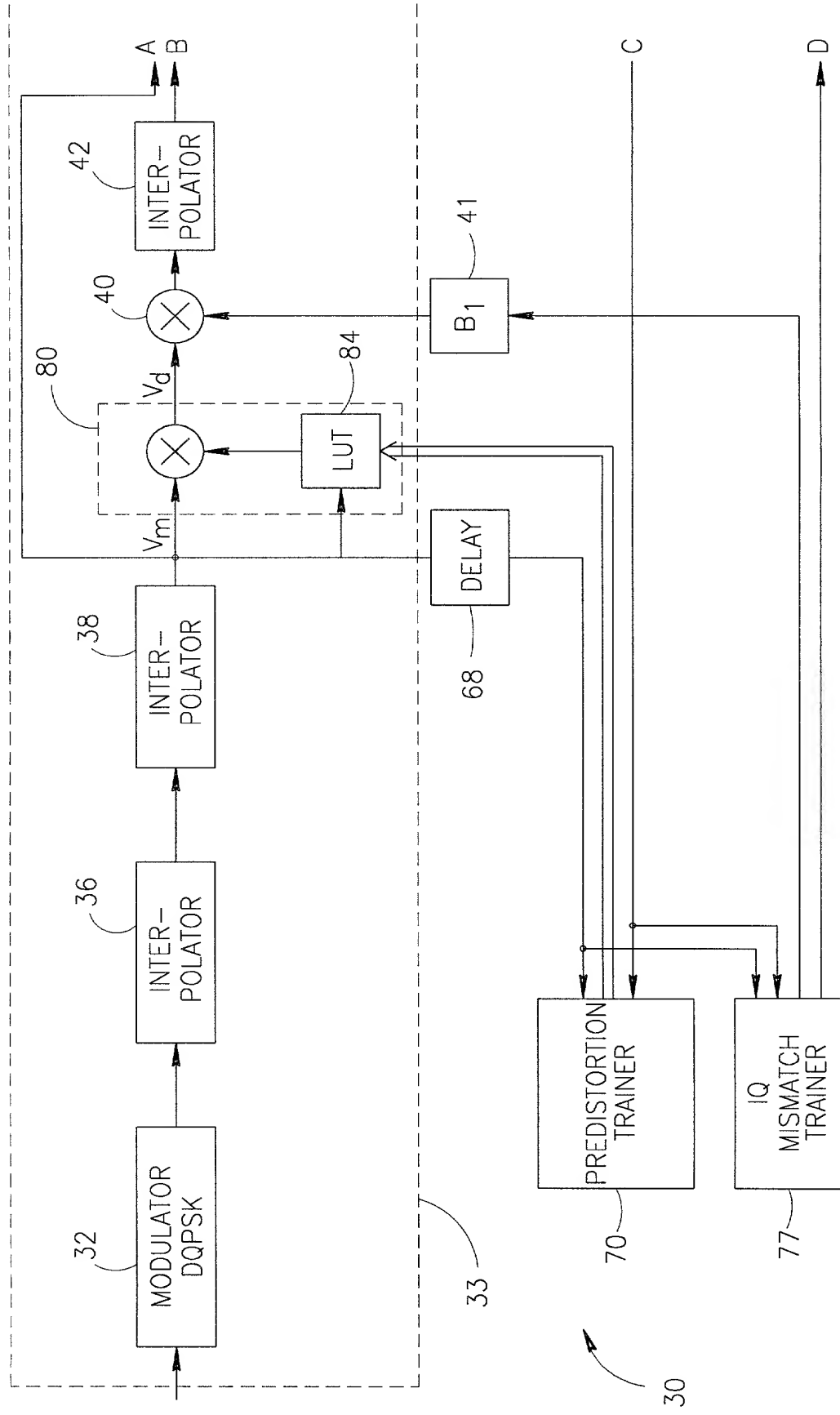


FIG. 1A



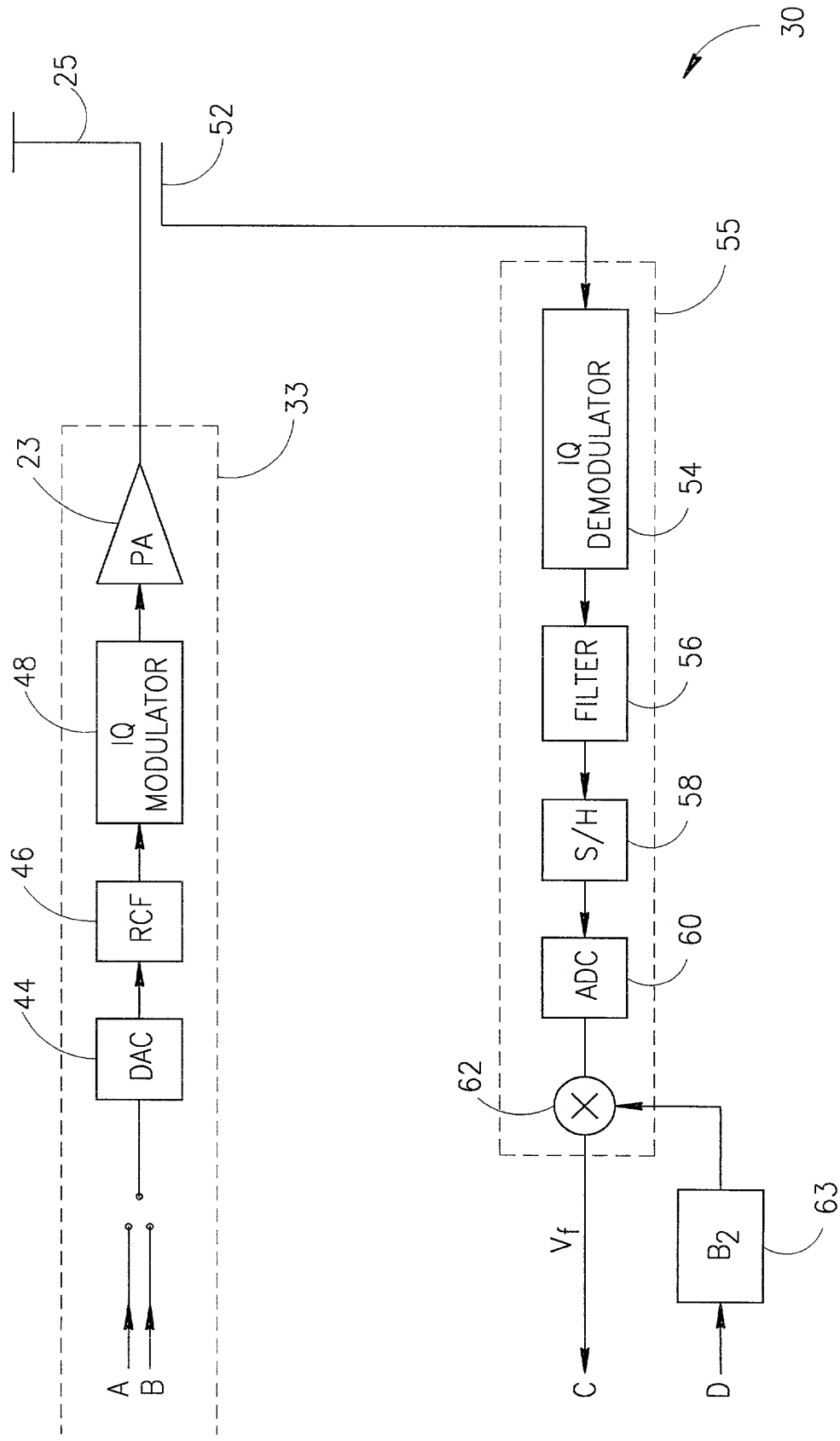


FIG.1B

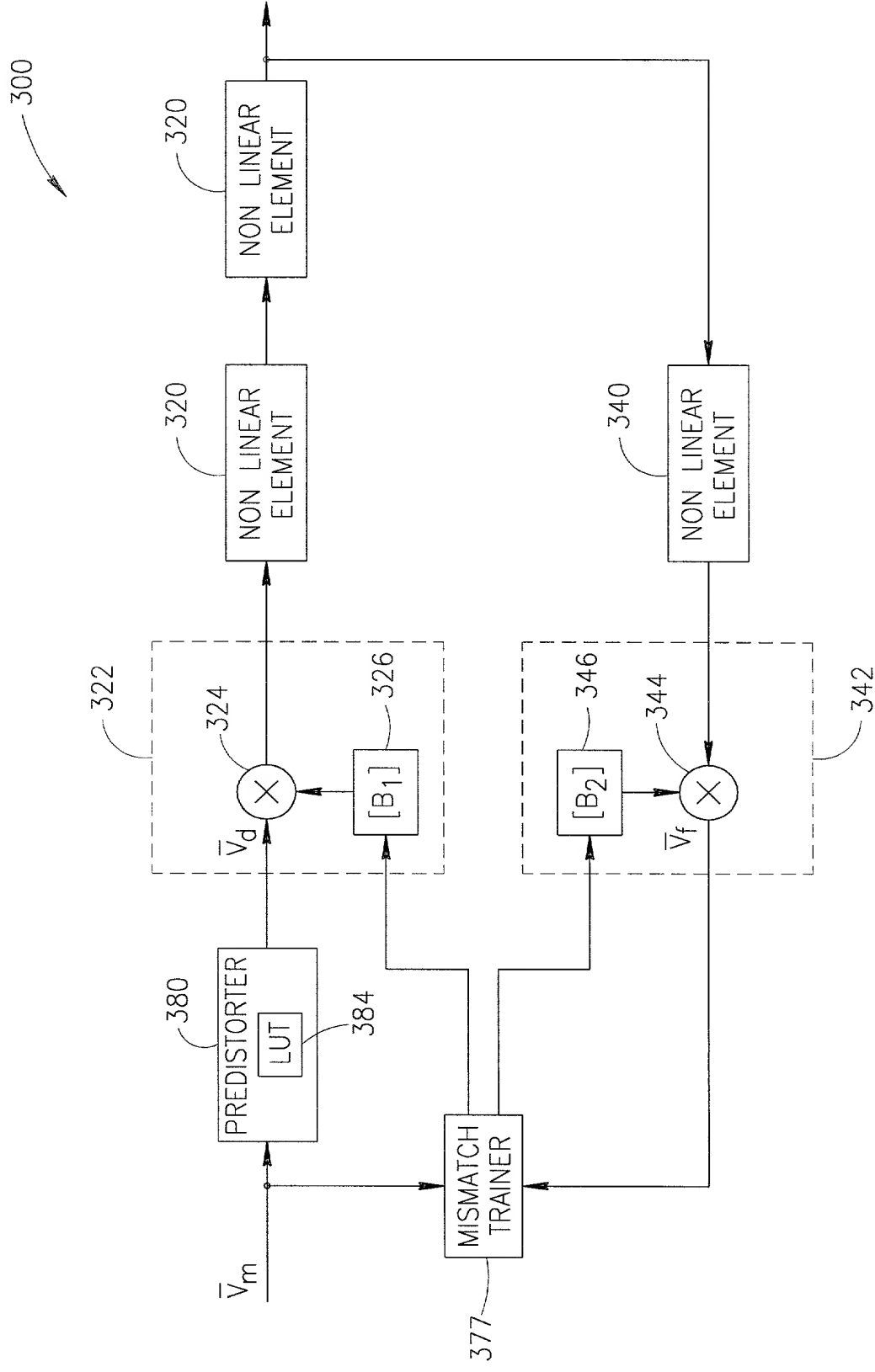


FIG.2



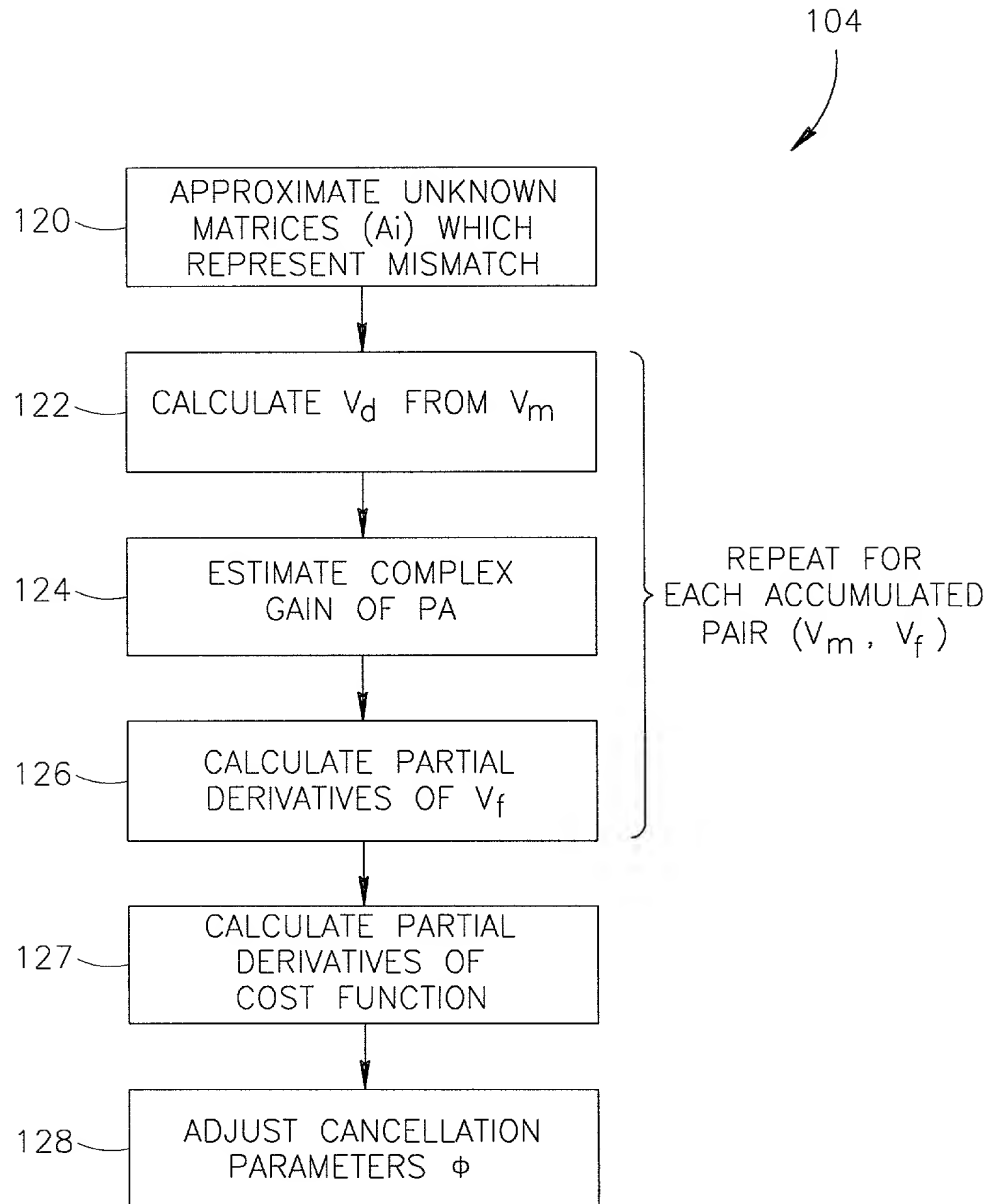


FIG.4

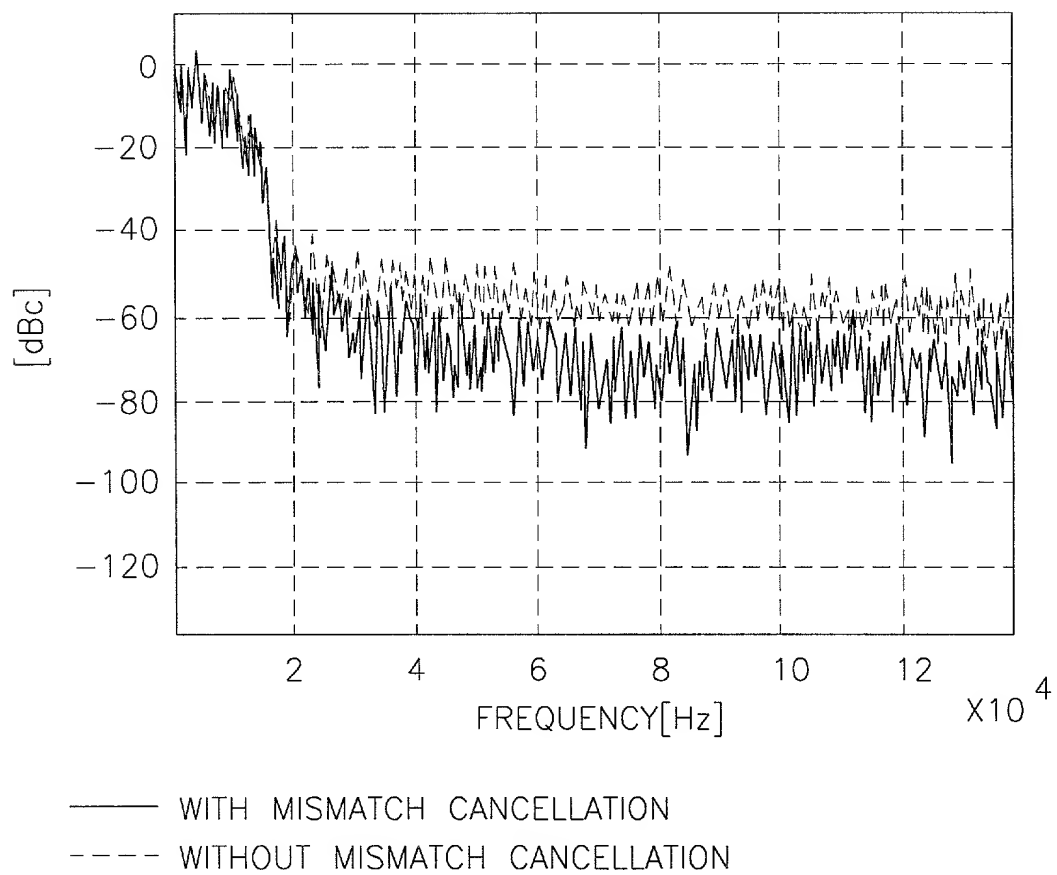


FIG.5

Docket No.  
162/01498

# Declaration and Power of Attorney For Patent Application

## English Language Declaration

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name,

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled

### IQ MISMATCH CANCELLATION

the specification of which

(check one)

☒ is attached hereto.

☐ was filed on \_\_\_\_\_ as United States Application No. or PCT International Application Number \_\_\_\_\_ and was amended on \_\_\_\_\_

(if applicable)

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose to the United States Patent and Trademark Office all information known to me to be material to patentability as defined in Title 37, Code of Federal Regulations, Section 1.56.

I hereby claim foreign priority benefits under Title 35, United States Code, Section 119(a)-(d) or Section 365(b) of any foreign application(s) for patent or inventor's certificate, or Section 365(a) of any PCT International application which designated at least one country other than the United States, listed below and have also identified below, by checking the box, any foreign application for patent or inventor's certificate or PCT International application having a filing date before that of the application on which priority is claimed.

Prior Foreign Application(s)

Priority Not Claimed

_____ (Number)	_____ (Country)	_____ (Day/Month/Year Filed)	<input type="checkbox"/>
_____ (Number)	_____ (Country)	_____ (Day/Month/Year Filed)	<input type="checkbox"/>
_____ (Number)	_____ (Country)	_____ (Day/Month/Year Filed)	<input type="checkbox"/>

I hereby claim the benefit under 35 U.S.C. Section 119(e) of any United States provisional application(s) listed below:

_____ (Application Serial No.)	_____ (Filing Date)
_____ (Application Serial No.)	_____ (Filing Date)
_____ (Application Serial No.)	_____ (Filing Date)

I hereby claim the benefit under 35 U. S. C. Section 120 of any United States application(s), or Section 365(c) of any PCT International application designating the United States, listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States or PCT International application in the manner provided by the first paragraph of 35 U.S.C. Section 112, I acknowledge the duty to disclose to the United States Patent and Trademark Office all information known to me to be material to patentability as defined in Title 37, C. F. R., Section 1.56 which became available between the filing date of the prior application and the national or PCT International filing date of this application:

_____ (Application Serial No.)	_____ (Filing Date)	_____ (Status) (patented, pending, abandoned)
_____ (Application Serial No.)	_____ (Filing Date)	_____ (Status) (patented, pending, abandoned)
_____ (Application Serial No.)	_____ (Filing Date)	_____ (Status) (patented, pending, abandoned)

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

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